INFORMATION DISCLOSURE STATEMENT BY APPLICANT

(use as many sheets as necessary)

Complete if Known	
Application Number	10/750,421
Filing Date	December 31, 2003
First Named Inventor	Yan Zhou
Group Art Unit	2646
Examiner Name	Singh, Ramnandan P.

Sheet	1	of	2	Attorney Docket Number	75622P006401

U.S. PATENT DOCUMENTS

Examiner Initials*	Cite No.[1]	U.S. Patent Document Number	Kind Code[2] (if known)	Name of Patentee or Applicant of Cited Document	Date of Publication of Cited Document MMM-DD-YYYY	Pages, Columns, Lines Where Relevant Passages or Relevant Figures Appear
R9		5,329,588	A	Wilcox	Jul-12-1994	
RP		5,930,340	A	Bell	Jul-27-1999	
RP		6,424,636	B1	Seaholtz, et al.	Jul-23-2002	
RP		6,895,040	B2	Zhou	May-17-2005	
RP		6,944,213	B2	Lee	Sep-13-2005	

FOREIGN PATENT DOCUMENTS

Examiner Initials*	Cite No.[1]	Foreign Patent Document Office[3]	Number[4]	Kind Code[5] (if known)	Name of Patentee or Applicant of Cited Document	Date of Publication of Cited Document MM-DD-YYYY	Pages, Columns, Lines Where Relevant Passages or Relevant Figures Appear	T[6]

Examiner Signature	Ram Ch	Date Considered	3/22/06

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OTHER PRIOR ART - NON PATENT LITERATURE DOCUMENTS

Examiner Initials*	Cite No.[1]	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T[2]
Rp		INFINEON TECHNOLOGIES, "Product Brief: GEMINAX Global Enhanced Multiport Integrated ADSL Transceiver", Infineon Technologies AG, Germany (2001)	
Ry		INFINEON TECHNOLOGIES, "Product Brief: GEMINAX Global Enhanced Multiport Integrated ADSL Transceiver", Infineon Technologies AG, Germany (2001)	
Ry		INFINEON TECHNOLOGIES, "Press Release: Infineon Technologies Introduces the IVAX Family - the First Chipset to Integrate Voice and Fullrate ADSL on a Single Line Card", Infineon Technologies AG, Germay (May 15, 2001)	
Ry		PESSL, PETER, ET AL. "A four channel Integrated Voice and ADSL Full-Rate Analog Front End", 27th European Solid-State Circuits Conference, Villach, Austria, September 18-20, 2001	

Examiner Signature	*Ram Sh*	Date Considered	3/22/08

*EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.

[1]Unique citation designation number. [2]Applicant is to place a check mark here if English language Translation is attached.